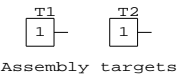
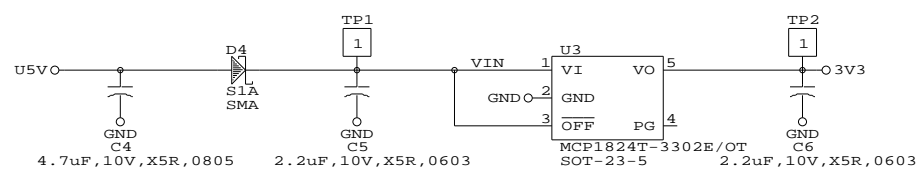
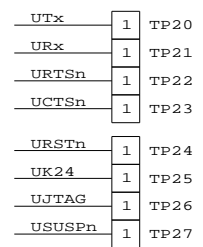
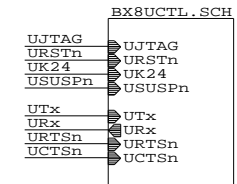


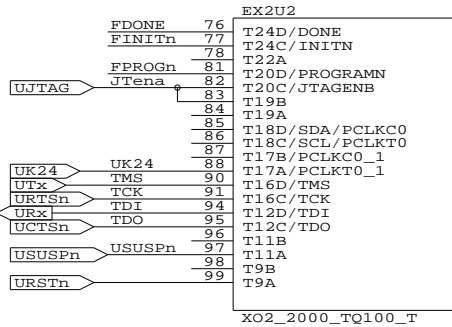
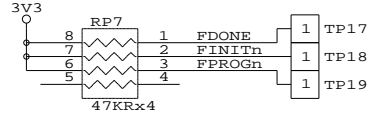
CBUS pins pull up approx 75K in input mode
 RTS defaults to output
 CTS defaults to input



BX8u.SCH

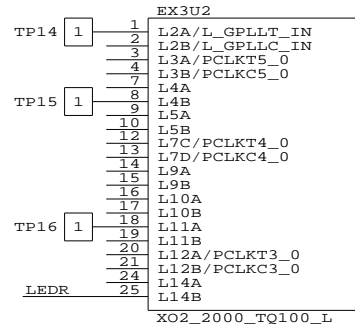
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PROGn/DONE/INITn have pullups during configuration
 JTAG pins except TDI have internal pullups

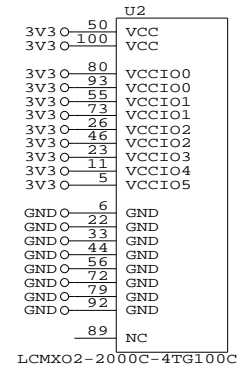


XO2_2000_TQ100_T

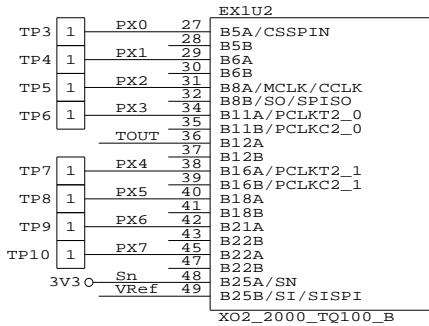
JTAGen Lo: pins are GPIO, Hi: pins are JTAG



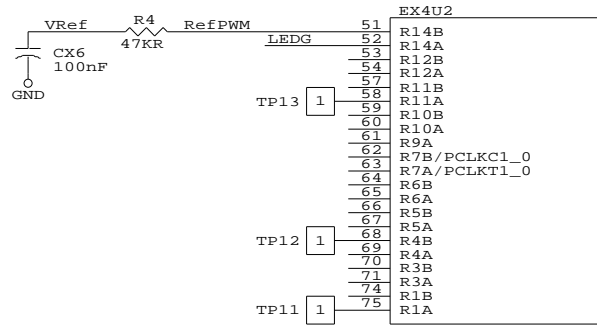
XO2_2000_TQ100_L



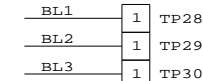
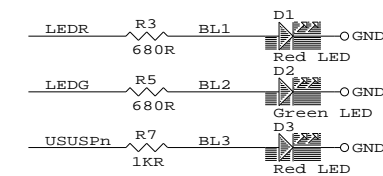
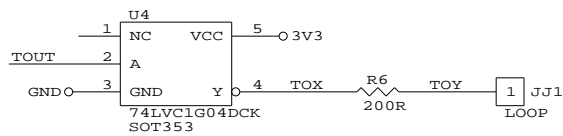
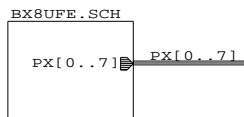
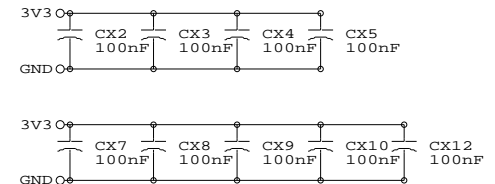
LCMXO2-2000C-4TG100C



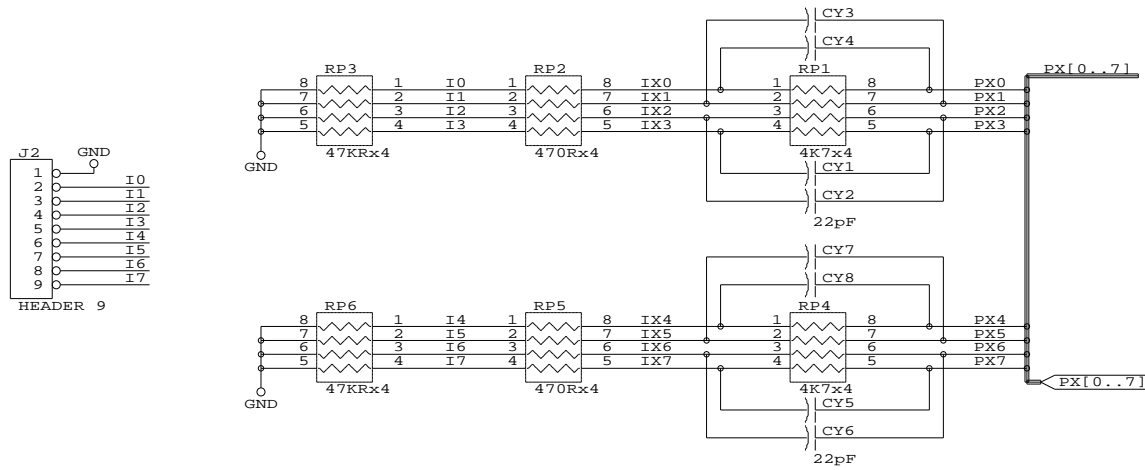
XO2_2000_TQ100_B



XO2_2000_TQ100_R



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Bugblat BX8u - Front End		
Size	Document Number	REV
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Date: September 14, 2014		Sheet 3 of 3

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